

FIG. 1

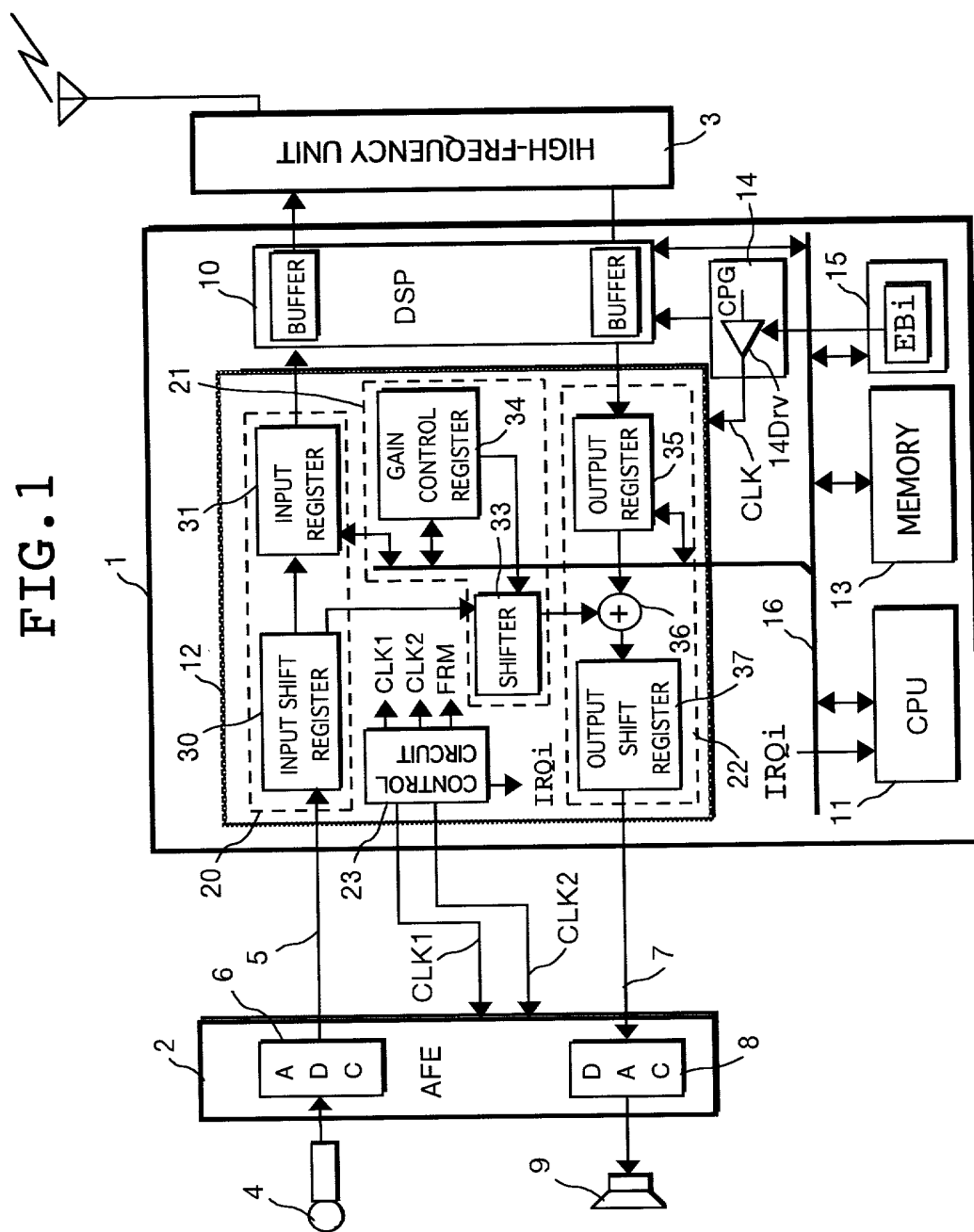


FIG. 2

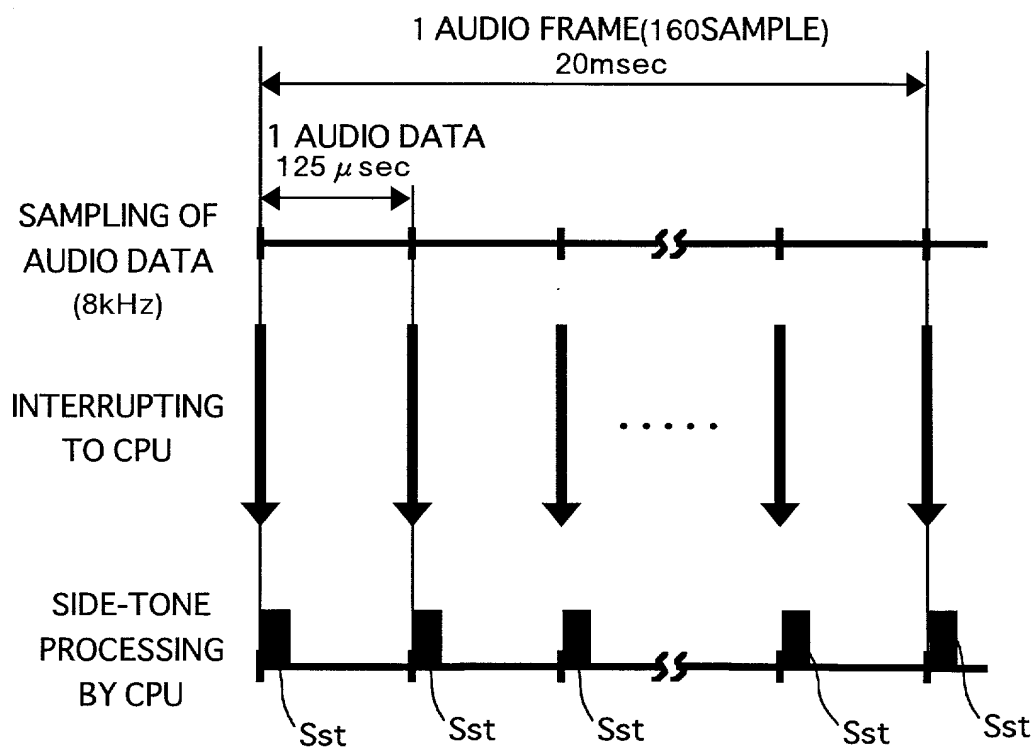


FIG. 3

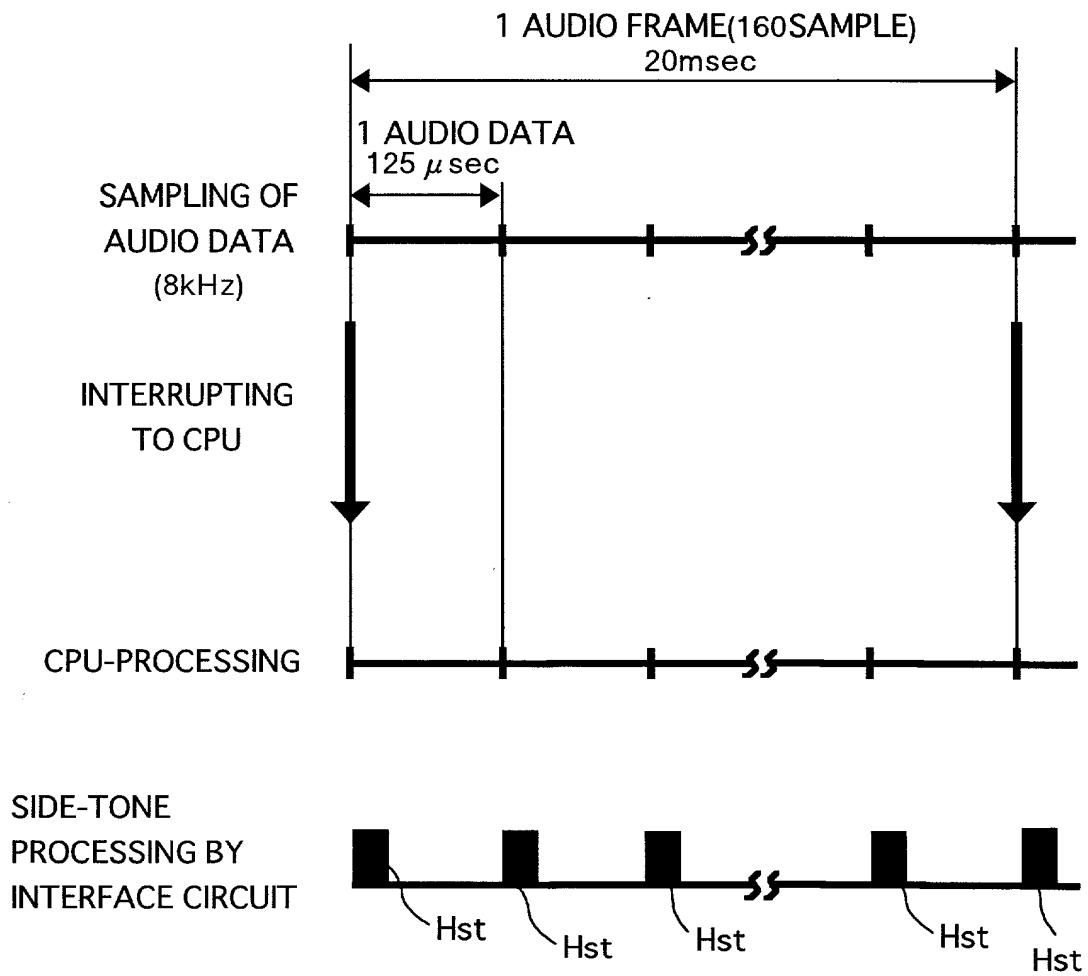


FIG. 4

| REGISTER NAME | ADDRESS | R / W (Read/Write) | ACCESS SIZE |
|---------------------------|------------|-----------------------|-------------|
| INPUT SHIFT REGISTER(30) | -- | -- | 16 bit |
| INPUT REGISTER(31) | 0x10000000 | R / W | 16 bit |
| OUTPUT SHIFT REGISTER(37) | -- | -- | 16 bit |
| OUTPUT REGISTER(35) | 0x10000002 | R / W | 16 bit |
| GAIN CONTROL REGISTER(34) | 0x10000004 | R / W | 8 bit |
| ⋮ | ⋮ | ⋮ | ⋮ |

FIG. 5

| GAIN CONTROL REGISTER(34) DATA | BIT SHIFT OPERATION DATA |
|-----------------------------------|-----------------------------|
| 0x00 | NONE |
| 0x01 | 1BIT |
| 0x02 | 2BIT |
| ⋮ | ⋮ |
| 0x0F | 15BIT |
| 0x10 | 16BIT |

FIG. 6

| No | INPUT SHIFT REGISTER(30) DATA | GAIN CONTROL REGISTER(34) DATA | OUTPUT REGISTER(35) DATA | SHIFTER DATA | OUTPUT SHIFT REGISTER(37) DATA |
|----|-------------------------------------|--------------------------------------|--------------------------------|-----------------|--------------------------------------|
| 1 | 0x00FF | 0x02 | 0x0F00 | 0x003F | 0x0F3F |
| 2 | 0x00FE | 0x02 | 0x0F10 | 0x003F | 0x0F4F |
| 3 | 0x008F | 0x02 | 0x0F00 | 0x0023 | 0x0F23 |
| • | • | • | • | • | • |
| • | • | • | • | • | • |
| • | • | • | • | • | • |
| • | • | • | • | • | • |

FIG. 7

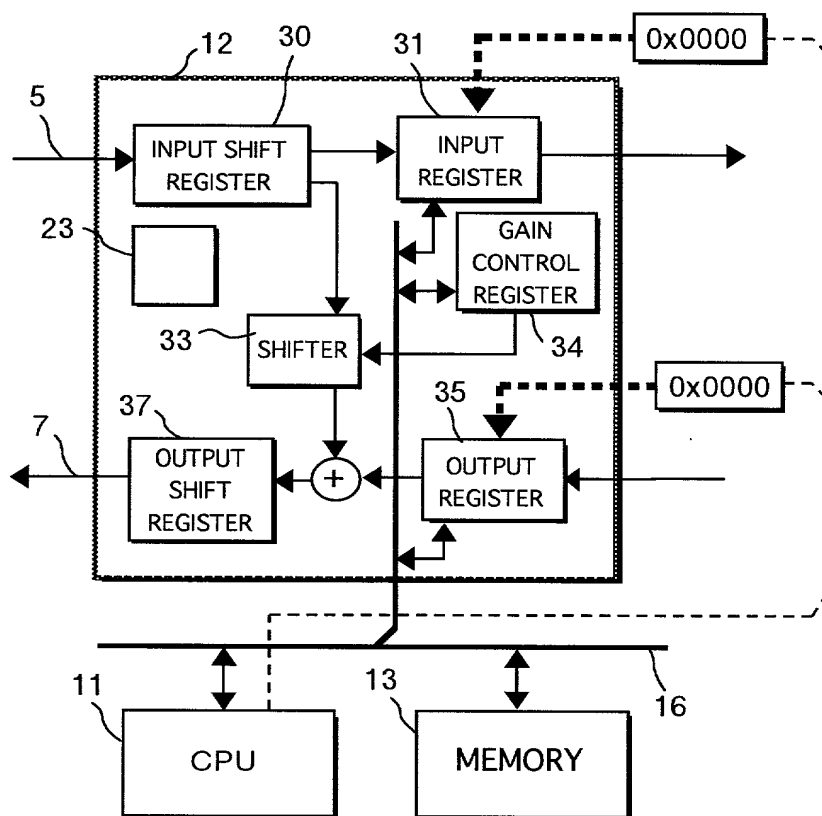


FIG. 8

